

Electronic Acknowledgement Receipt

EFS ID:	3500096
Application Number:	10757193
International Application Number:	
Confirmation Number:	3577
Title of Invention:	METHOD FOR MANUFACTURING RESIST PATTERN AND METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE
First Named Inventor/Applicant Name:	Shunpei Yamazaki
Correspondence Address:	COOK, ALEX, McFARRON, MANZO, CUMMINGS & MEHLER, LTD. SUITE 2850 200 WEST ADAMS STREET CHICAGO IL 60606 US 3122368500 -
Filer:	Mark James Murphy/Cristine Noll
Filer Authorized By:	Mark James Murphy
Attorney Docket Number:	0553-0394
Receipt Date:	23-JUN-2008
Filing Date:	14-JAN-2004
Time Stamp:	14:09:12
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$ 1770

RAM confirmation Number	8037				
Deposit Account	501039				
Authorized User					
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File Listing:					
Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Issue Fee Payment (PTO-85B)	IssueFeeSEL394.pdf	158183	no	1
			f84a983ee6485b140b23cd22708228e76b3ebdd4		
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8492	no	2
			7c8f714cd311c6ffcbdb8a5c425707a1a7fe78f2		
Warnings:					
Information:					
Total Files Size (in bytes):			166675		
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